

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(s):	Heinen	CONF. NO.:	5365
SERIAL NO.:	10/557,104	ART UNIT:	2857
FILING DATE:	27 November 2006	EXAMINER:	Charioui, M.
TITLE:	INTEGRATED CIRCUIT WITH BIT ERROR TEST CAPABILITY		
ATTORNEY DOCKET NO.:	20031035-02		

Mail Stop Amendment  
Commissioner of Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**AMENDMENT**

**I. INTRODUCTION**

This is in response to the Office Action mailed 19 November 2007 for this application.

Reconsideration of the rejection of the claims is respectfully solicited in light of the following amendment and remarks.